

Asymmetric thermal transport in a trilayer van der Waals heterostructure

Received: 18 June 2025

Accepted: 29 March 2026

Published online: 5 May 2026



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The heat generated within integrated circuit architectures needs to be efficiently transferred to the package surface through multilayer interfaces in the out-of-plane direction. At the same time, reverse heat flow needs to be blocked to prevent the failure of thermally sensitive components. Achieving the necessary asymmetric phonon transport in the out-of-plane direction is, however, challenging. Here we report a trilayer van der Waals heterostructure that offers asymmetric thermal transport. This thermal Janus crystal is made of molybdenum disulfide (MoS_2)/molybdenum sulfide selenide (MoSSe)/tungsten diselenide (WSe_2) and has a thickness of less than 5 nm. By precisely adjusting the twist angles at the $\text{MoS}_2/\text{MoSSe}$ and $\text{MoSSe}/\text{WSe}_2$ interfaces, the asymmetric property—defined as the relative change in the interfacial thermal conductance under opposite temperature gradients—can be tuned from 23% to 104%. Molecular dynamics simulations indicate that the contributions of in-plane and out-of-plane phonon modes at the $\text{MoS}_2/\text{MoSSe}$ and $\text{MoSSe}/\text{WSe}_2$ interfaces are different, which leads to asymmetry in the heat transport. In a thermal test with a field-effect transistor and using a 58-mW microwire heater, the surface temperature is reduced by 3.9 K when heat flows from WSe_2 to MoS_2 compared with when the heat flows in the opposite direction through the heterostructure.

In three-dimensional integrated circuits, heat is primarily conducted through multiple interfaces in the out-of-plane direction from internal high-electron-mobility transistors to the external packaging. Effective thermal management, thus, requires efficient heat conduction from the interior to the exterior and blocking reverse heat flow to protect heat-sensitive components. However, achieving nanoscale control of interfacial thermal conductivity and out-of-plane asymmetric phonon transport remains challenging. Thermal rectifiers based on Tesla valve structures, nanopores/nanoparticles and metamaterials^{1–5} have been developed. However, these devices typically range in size from tens to hundreds of micrometres and are limited to manipulating heat flow only in the in-plane direction.

Janus crystals—material systems that exhibit different properties on two opposing sides or surfaces^{6–13}—could provide a solution to overcome the limitations of size and direction in thermal management technology. Molybdenum sulfide selenide (MoSSe) is a recently developed two-dimensional (2D) Janus material that offers symmetry breaking in three atomic layers and exhibits a giant nonlinear optical response and an anomalous photovoltaic effect^{14–16}. However, the thickness of a MoSSe monolayer is far smaller than the phonon mean free path—the average distance a phonon can travel before being scattered—which restricts the control of perpendicular phonon transport across the monolayer.

In this Article, we report a thermal Janus crystal made of molybdenum disulfide (MoS_2)/molybdenum sulfide selenide (MoSSe)/tungsten

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diselenide (WSe₂) trilayer van der Waals heterostructure, which is inspired by the control of electronic energy band and correlated states in magic-angle 2D dimensional materials with moiré superlattices^{17–26}. By finely tuning the twist angles at the MoS₂/MoSe and MoSe/WSe₂ interfaces, the spacing between adjacent atoms on both sides of MoSe and the interlayer van der Waals force can be modulated^{27,28}, enhancing control over phonon transport.

The asymmetric thermal transport property (η) is quantified as the relative change of thermal conductance (G) in the opposite temperature gradient directions. We develop an X-shaped sensor to measure the thermal conductance in different vertical heat flow directions. By independently changing the twist angles at the MoS₂/MoSe and MoSe/WSe₂ heterointerfaces, η can be tuned from 23% to 104%. Molecular dynamics simulations show that the contributions of in-plane and out-of-plane phonon modes to the interfacial thermal conductance at two interfaces are different, which results in asymmetric phonon transport. We measure the surface temperature of the trilayer material placed on a microwire heater by using a thermoreflectance thermal imaging (TTI) method. The measured surface temperature is 3.9 K lower when heat flows from WSe₂ to MoS₂ than when it flows in the opposite direction at an input power of 58 mW.

Fabrication of X-shaped sensors

To measure the forward and reverse out-of-plane thermal conductance of the MoS₂/MoSe/WSe₂ trilayer heterostructure, we designed and fabricated X-shaped sensors (Fig. 1). First, a bottom gold nanosensor (1 $\mu\text{m} \times 15 \mu\text{m}$, 50 nm thick) was deposited onto a SiO₂/Si substrate through physical vapour deposition (PVD) (Fig. 1a). Subsequently, three triangular single-crystalline transition-metal dichalcogenide (TMD) monolayers (MoS₂, MoSe and WSe₂) were mechanically transferred with precisely controlled twist angles θ_1 and θ_2 (Methods provides details on Janus MoSe fabrication). The transfer process of a triangular TMD monolayer involved the following. (1) A polymethyl methacrylate (PMMA) film was spin coated on top of a single-crystalline MoS₂ monolayer substrate. (2) Using the microknife (MKS-15) equipped on the micromanipulation system, a single-crystalline MoS₂ triangular domain supported by PMMA was cut off and removed from the substrate. (3) A pair of microprobes were used to mechanically transfer the monolayer MoS₂ crystal domain onto the target surface. Both position and orientation of the triangular crystal domain were meticulously adjusted. To precisely control the twist angle, one corner of the PMMA film was anchored by probe A, whereas probe B was used to nudge the opposite corner and turn the sample to the desired twist angle (Fig. 1k; Supplementary Video 1 provides details). (4) The PMMA layer was removed through immersion into warm butanone. Annealing at 400 °C for 8 h was conducted to eliminate possible contaminations or residues on the sample. The same procedure was repeated to transfer MoSe and WSe₂ (Methods).

The other gold nanosensor perpendicular to the bottom sensor was deposited on top of the trilayer TMD sample, creating an Au/MoS₂/MoSe/WSe₂/Au stack from bottom to top (Fig. 1e,j). In particular, a 3 $\mu\text{m} \times 3 \mu\text{m} \times 50 \text{ nm}$ SiO₂ square was fabricated at the centre of the X-shaped sensors to support the MoS₂/MoSe/WSe₂ trilayer. The remaining TMD materials outside the square were etched away. The whole X-shaped sensors were fully suspended from the substrate by using XeF₂ gas etching (Fig. 1o,p), to achieve the highest possible thermal measurement sensitivity.

To further evaluate the quality of interface between stacked TMD layers, the aberration-corrected transmission electron microscopy (TEM) was used to examine the cross-sectional view of the trilayer heterostructure (Fig. 1n). The images revealed the good flatness and cleanliness of the heterogeneous interfaces. The interlayer distance between MoS₂ and MoSe and that between MoSe and WSe₂ were measured to be 0.68 nm and 0.67 nm, respectively, aligning with the theoretical expectation of 0.65 nm (ref. 27). This result demonstrates

the good quality of the heterointerface achieved through the used mechanical transfer technology. In particular, the current method used to transfer monolayer TMD single-crystal domain is generalizable and can be applied to transfer and stack various 2D materials. Using this approach, we have fabricated different stacked structures for comparison, like Au/MoS₂/Au, Au/MoS₂/MoSe/Au and Au/MoS₂/WSe₂/Au.

Atomic-scale stacking characterization

For the MoS₂/MoSe and MoSe/WSe₂ heterostructures, the twist angle between the two layers of 2D materials was defined as θ (Fig. 2a), representing the average angle of the corresponding edges of the top and bottom triangles. This angle was measured using optical microscopy (OM) with an error of approximately 0.3° (Supplementary Section 3). For the MoS₂/MoSe/WSe₂ trilayer heterostructure samples, we used MoSe as the benchmark and recorded the twist angle between MoSe and MoS₂ and that between MoSe and WSe₂ as θ_1 and θ_2 , respectively (Fig. 1l). To assess the precision of the twist angle measurements obtained from OM, the polarization-resolved second-harmonic generation (SHG) spectra were used to determine the lattice orientation of the bilayer twisted MoS₂ (Fig. 2b–d). In the non-overlapping regions of the upper and lower layers of MoS₂, the SHG signal exhibits distinct six-fold rotational symmetry, attributable to the three-fold rotational symmetry of the MoS₂ lattice. The directions of the maximum SHG signal intensity correspond to the armchair crystallographic directions. Consequently, the twist angles could be extracted by fitting the SHG patterns as 0.09°, 14.23° and 29.18° in Fig. 2e–g, respectively, which is consistent with the corresponding values of 0.1°, 14.5° and 29.6° obtained through the OM measurements. This agreement demonstrates the reliability of the angular measurements in our mechanical transfer technology.

Furthermore, we examined the overlapping region of MoS₂ using scanning TEM. The atomically resolved moiré patterns presented in Fig. 2h–j confirm the high crystallinity and exceptional transfer efficacy of TMD. The moiré fringe periodicity in twisted bilayer MoS₂ can be computed as follows:

$$L = \frac{a}{\sqrt{2(1 - \cos \theta)}}, \quad (1)$$

where L is the moiré length and a represents the lattice constant of MoS₂, which is 0.3125 nm. θ denotes the twist angle. The measured moiré periodicities (Fig. 2h–j) are 2.79 nm, 1.47 nm and 0.64 nm, respectively, corresponding to theoretical twist angles of 6.4°, 12.2° and 28.5°. The Raman mappings were also performed for the MoSe, MoS₂, WSe₂ and MoSe₂ monolayers (Fig. 2k), which indicated the large-area uniformity of the samples. Their characteristic peak positions of the A_{1g} modes for the four samples were located at 290.5 cm^{−1}, 406.5 cm^{−1}, 260.1 cm^{−1} and 241.3 cm^{−1}, respectively, whereas the corresponding E_{2g} mode characteristic peak positions were observed at 358.6 cm^{−1}, 384.6 cm^{−1}, 250.8 cm^{−1} and 292.6 cm^{−1}, consistent with the data reported in the existing literature^{28–32} (Supplementary Fig. 2). Although MoSe₂/WSe₂ and MoS₂/MoSe/WSe₂ heterostructures exhibited similar colours in the OM images, the distinct Raman peak characteristics confirmed their layer-by-layer stacking configurations, consistent with our cross-sectional aberration-corrected TEM results (Fig. 1n).

Out-of-plane thermal conductance measurement and asymmetry heat transport characteristics

Using the X-shaped sensor approach, we measured the out-of-plane thermal conductance G of all the MoS₂/MoSe, MoSe₂/WSe₂ and MoS₂/MoSe/WSe₂ heterostructures under different heat flow directions, similar to the H-type sensor for measuring the in-plane thermal conductivity in our previous work³³. In this setup, the bottom and top sensors of the suspended device functioned as a ‘Joule heater’ and a precise ‘resistance thermometer’, respectively. The role of the heater

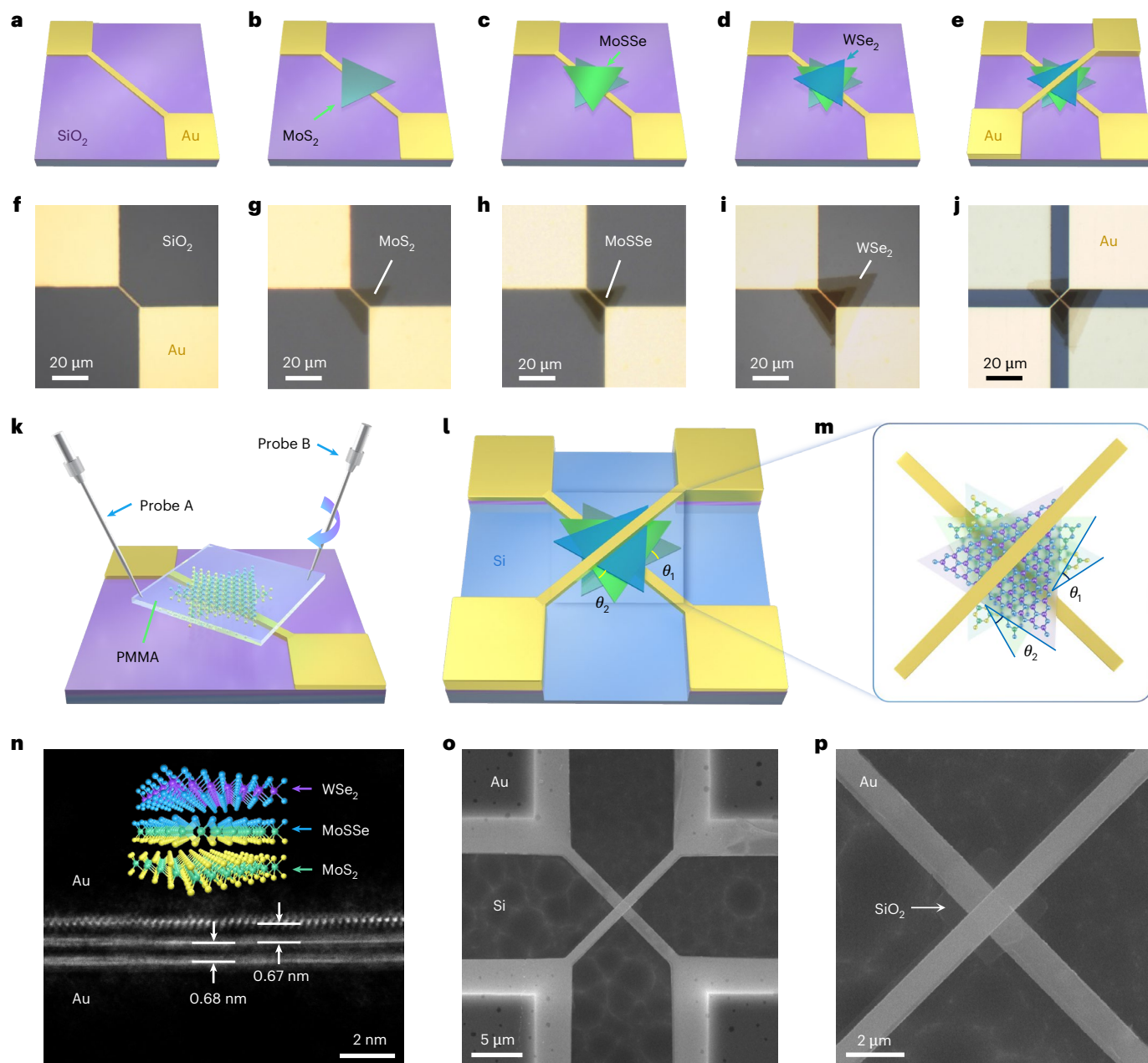


Fig. 1 | Fabrication of X-shaped sensors for measuring the out-of-plane thermal conductance of trilayer $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ heterostructure. **a**, Deposition of the bottom gold nanosensor onto the SiO_2/Si substrate through PVD. **b**, Mechanical transfer of a triangular single-crystalline MoS_2 monolayer onto the bottom sensor. **c**, Mechanical transfer of a triangular single-crystalline Janus MoSSe monolayer onto MoS_2 with a twist angle θ_1 . **d**, Mechanical transfer of a triangular single-crystalline WSe_2 monolayer onto MoSSe with a twist angle θ_2 . **e**, Deposition of the top gold nanosensor onto WSe_2 through PVD. **f–j**, OM images

from steps shown in **a–e** (**f–j**, respectively). **k**, Precise control of θ_1 and θ_2 between two neighbouring TMD monolayers by using a pair of microprobes. **l**, Etch the SiO_2 layer and exposed Si substrate by using reactive ion etching and XeF_2 gas, respectively, to suspend the X-shaped sensors together with a trilayer sample in between. **m**, Schematic of the core structure in the middle of the X-shaped sensors. **n**, Aberration-corrected TEM image of the cross-section of the $\text{Au}/\text{MoS}_2/\text{MoSSe}/\text{WSe}_2/\text{Au}$ stacking layers. **o, p**, SEM images of suspended X-shaped sensors with trilayer TMD samples supported by a $3\ \mu\text{m} \times 3\ \mu\text{m} \times 50\ \text{nm}$ SiO_2 square.

and thermometer can be switched by simply changing the electrical current through each independent sensor, allowing the heat flow direction to be reversed (Fig. 3a). For a constant heating power, higher out-of-plane thermal conductance results in a greater temperature rise in the thermometer sensor. Using the measured heating power and temperature rises as input, the thermal conductance can be calculated via three-dimensional thermal simulations of the heat conduction equation (Methods). Before thermal measurement, the out-of-plane electrical transport properties of all heterostructure samples were measured. The drain–source current–voltage ($I_{\text{ds}}-V_{\text{ds}}$) curve (Fig. 3b)

indicated no appreciable electrical rectification, probably because of the narrow depletion layer within the heterojunction, which was only several atoms thick and, thus, did not generate a sufficient built-in electric field³⁴. Moreover, when V_{ds} was below 0.2 V, the interfacial resistance for all heterostructures remained above 150 Ω , substantially higher than the resistance of the gold sensor (approximately 15.6 Ω). This confirms no direct contact between the top and bottom sensors and no electrical breakdown within the heterostructure. Extreme electrical tests on the $\text{Au}/\text{MoS}_2/\text{Au}$ stacked structure (Supplementary Fig. 3) revealed that the resistance between two sensors was maintained stable above

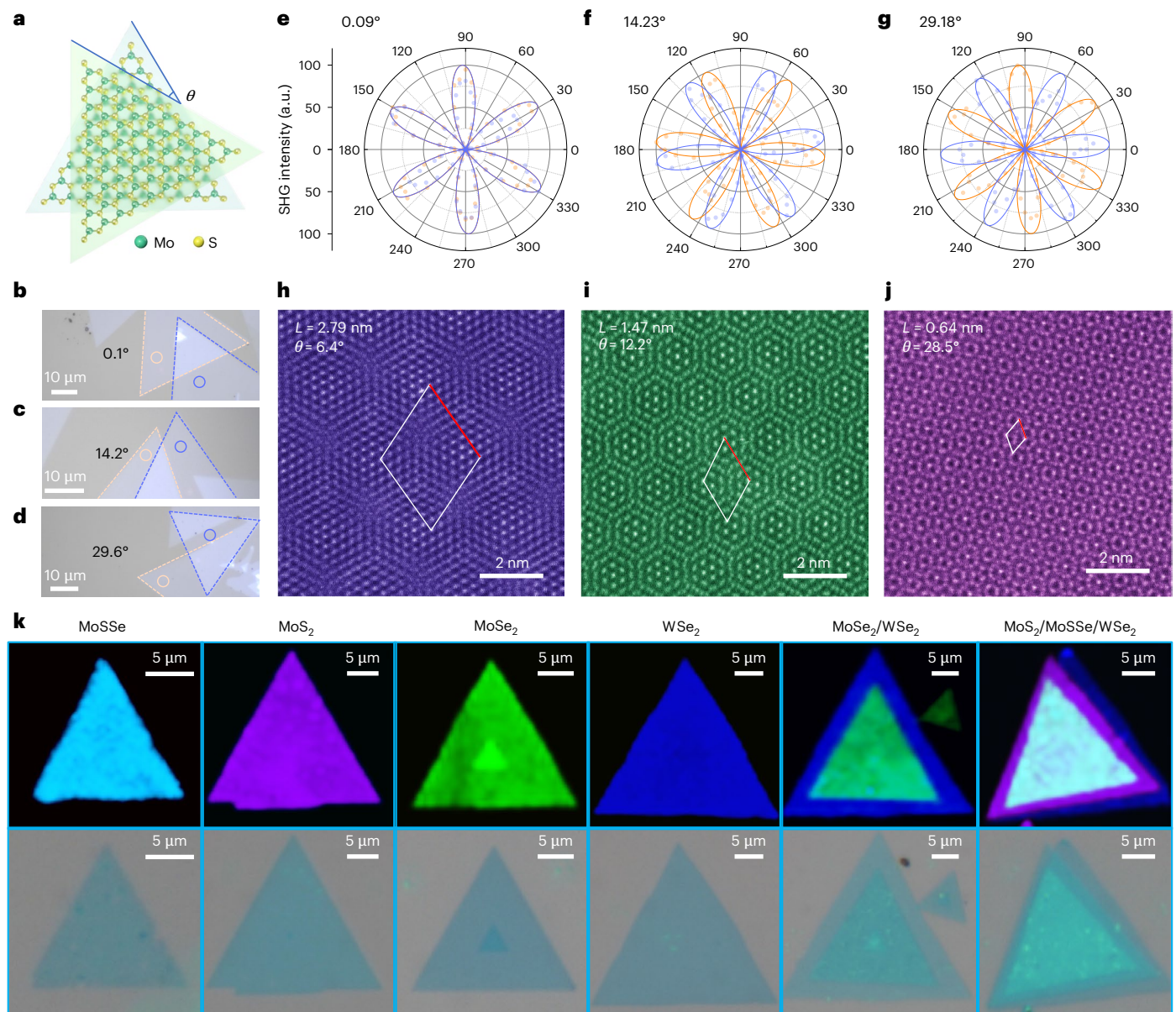


Fig. 2 | Accurate characterization of the twist angle and moiré patterns in twisted heterostructures. **a**, Schematic of θ for the $\text{MoS}_2/\text{MoS}_2$ bilayer. **b–d**, OM images of three triangular $\text{MoS}_2/\text{MoS}_2$ bilayer samples and the angle measurement results. **e–g**, Polarization-resolved SHG characterization results corresponding to the regions shown in **b–d** (**e–g**, respectively), with the

measurement area indicated by the circles in **b–d**. **h–j**, Scanning TEM images of $\text{MoS}_2/\text{MoS}_2$ heterostructures with three twist angles. The white rhombuses indicate moiré periodic stripes and the red edges represent the moiré lengths L . **k**, Raman spatial mapping images of monolayer MoS_2 , MoSe_2 and WSe_2 samples, as well as $\text{MoSe}_2/\text{WSe}_2$ and $\text{MoS}_2/\text{MoSe}_2/\text{WSe}_2$ heterostructures.

130 Ω within a voltage range below 189 mV (Supplementary Video 2). To avoid electrical breakdown, the voltage applied to the gold sensors in the subsequent measurement of the out-of-plane thermal conductance was below 100 mV. Moreover, detailed circuit analysis further confirms the absence of leakage current during thermal measurements (Supplementary Fig. 5), precluding Joule heating arising from contact resistance and ensuring measurement reliability.

All the thermal measurements were conducted in a high-vacuum chamber (pressure, $<10^{-3}$ Pa) to avoid convective heat loss. The radiative heat transfer can be neglected, too (Supplementary Section 3). The overlapping region between the top and bottom sensors was only $1 \mu\text{m} \times 1 \mu\text{m}$, that is, the same region in which the heterostructure sample was positioned, much smaller than the area of the sensor ($15 \mu\text{m} \times 1 \mu\text{m}$). Therefore, the surface temperature distribution within the overlapping region was essentially uniform (within 0.15 K, as

confirmed by a finite element simulation). Consequently, heat transfer predominantly occurred in the out-of-plane direction across the heterostructure and could be modelled using a one-dimensional thermal resistance network. In general, the total thermal resistance, $1/G_{\text{eff}}$, includes the interfacial thermal resistance $1/G_s$ between Au and TMD and $1/G$ between TMD layers:

$$1/G_{\text{eff}} = 1/G_s + 1/G. \quad (2)$$

The calibration of G_s was conducted using three specifically designed stacked structures: $\text{Au}/\text{MoS}_2/\text{Au}$, $\text{Au}/\text{MoSe}_2/\text{Au}$ and $\text{Au}/\text{WSe}_2/\text{Au}$. A comprehensive description of the calibration methodology is provided in Supplementary Section 1. At the ambient temperature (293 K), the measured values are as follows: $G_{\text{Au}/\text{MoS}_2}$ is $28.48 \pm 2.28 \text{ MW m}^{-2} \text{ K}^{-1}$, $G_{\text{Au}/\text{MoSe}_2}$ is $33.92 \pm 2.72 \text{ MW m}^{-2} \text{ K}^{-1}$ and $G_{\text{Au}/\text{WSe}_2}$ is

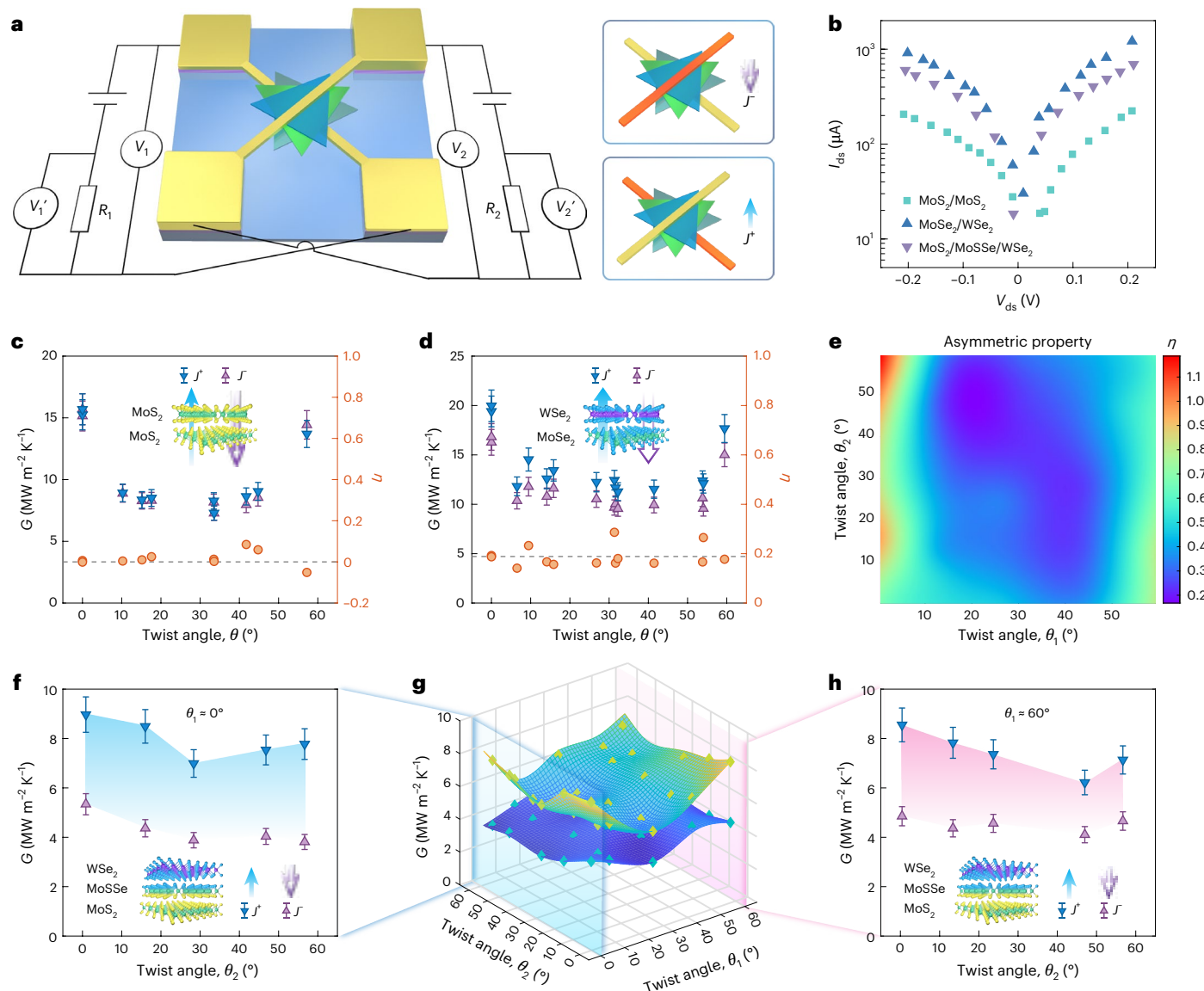


Fig. 3 | Out-of-plane thermal conductance measurement circuit and the asymmetric heat transport characteristics of $\text{MoS}_2/\text{MoS}_2$, $\text{MoSe}_2/\text{WSe}_2$, and $\text{MoS}_2/\text{MoSe}_2/\text{WSe}_2$ heterostructures. **a**, Schematic of the electrical circuit for measuring the out-of-plane thermal conductance using the X-shaped sensors. V_1 and V_2 measure the voltage of the bottom and top sensors, respectively. V_1' and V_2' correspondingly measure the electrical current in each loop. The blue solid arrow indicates the heat flow direction J' from the bottom to the top sensor, and the purple hollow arrow indicates the heat flow direction J from the top to the bottom sensor. **b**, I_{ds} – V_{ds} characterization in the out-of-plane direction of bilayer $\text{MoS}_2/\text{MoS}_2$, $\text{MoSe}_2/\text{WSe}_2$ and trilayer $\text{MoS}_2/\text{MoSe}_2/\text{WSe}_2$ heterostructures. **c**, **d**, Variations in the out-of-plane thermal conductance of bilayer $\text{MoS}_2/\text{MoS}_2$

(**c**) and $\text{MoSe}_2/\text{WSe}_2$ (**d**) heterostructures in two opposite heat flow directions with θ , respectively. **e**, Variations in η of trilayer $\text{MoS}_2/\text{MoSe}_2/\text{WSe}_2$ heterostructure with twist angles θ_1 and θ_2 . **g**, Variations in the out-of-plane thermal conductance of trilayer $\text{MoS}_2/\text{MoSe}_2/\text{WSe}_2$ heterostructure in two opposite heat flow directions with θ_1 and θ_2 . **f**, **h**, Variations in thermal conductance with θ_2 for $\theta_1 \approx 0^\circ$ and $\theta_1 \approx 60^\circ$, respectively. The shaded area highlights the difference in interfacial thermal conductance under J' and J heat flow conditions. Each data point in **c**, **d**, **f** and **h** represents an independent measurement result for a single sample. The vertical lines at each data point indicate a combined standard uncertainty of $\pm 8\%$. A detailed calculation of the uncertainty can be found in Supplementary Section 3.

$21.16 \pm 1.70 \text{ MW m}^{-2} \text{ K}^{-1}$, consistent with the values reported in other studies^{35–38}, confirming the reliability of the X-shaped sensor measurement approach.

By subtracting G_s from G_{eff} , the interfacial thermal conductance G between $\text{MoS}_2/\text{MoS}_2$ layers was determined as a function of θ (Fig. 3c). The dependence of G on the twist angle was symmetric around $\theta = 30^\circ$, consistent with the three-fold rotational symmetry of the MoS_2 crystal. The effective range of the twist angle was 0° – 30° (ref. 28). Within this range, G reached its maximum value of $15.7 \text{ MW m}^{-2} \text{ K}^{-1}$ at $\theta = 0.3^\circ$, after which it rapidly decreased with an increase in the twist angle. Conversely, from 10° to 30° , G remained almost unchanged, consistent

with prior experimental results by time-domain thermoreflectance and micro-Raman methods^{28,39,40}. The thermal conductance across the interface showed minimal variation with different heat flow directions. The asymmetric property of heat transfer (η) is equal to the ratio of G in two opposite heat flow directions minus one ($\eta = G^+/G^- - 1$). η remained consistently below 3% at the most investigated angle, indicating no asymmetry of heat transport effect, aligning with theoretical predictions. For the $\text{MoSe}_2/\text{WSe}_2$ heterostructure, the variation in G with twist angle was consistent with that of the $\text{MoS}_2/\text{MoS}_2$ heterostructure (Fig. 3d). In the heat flow direction from MoSe_2 to WSe_2 (J' direction), the interfacial thermal conductance G at 14 different angles were all

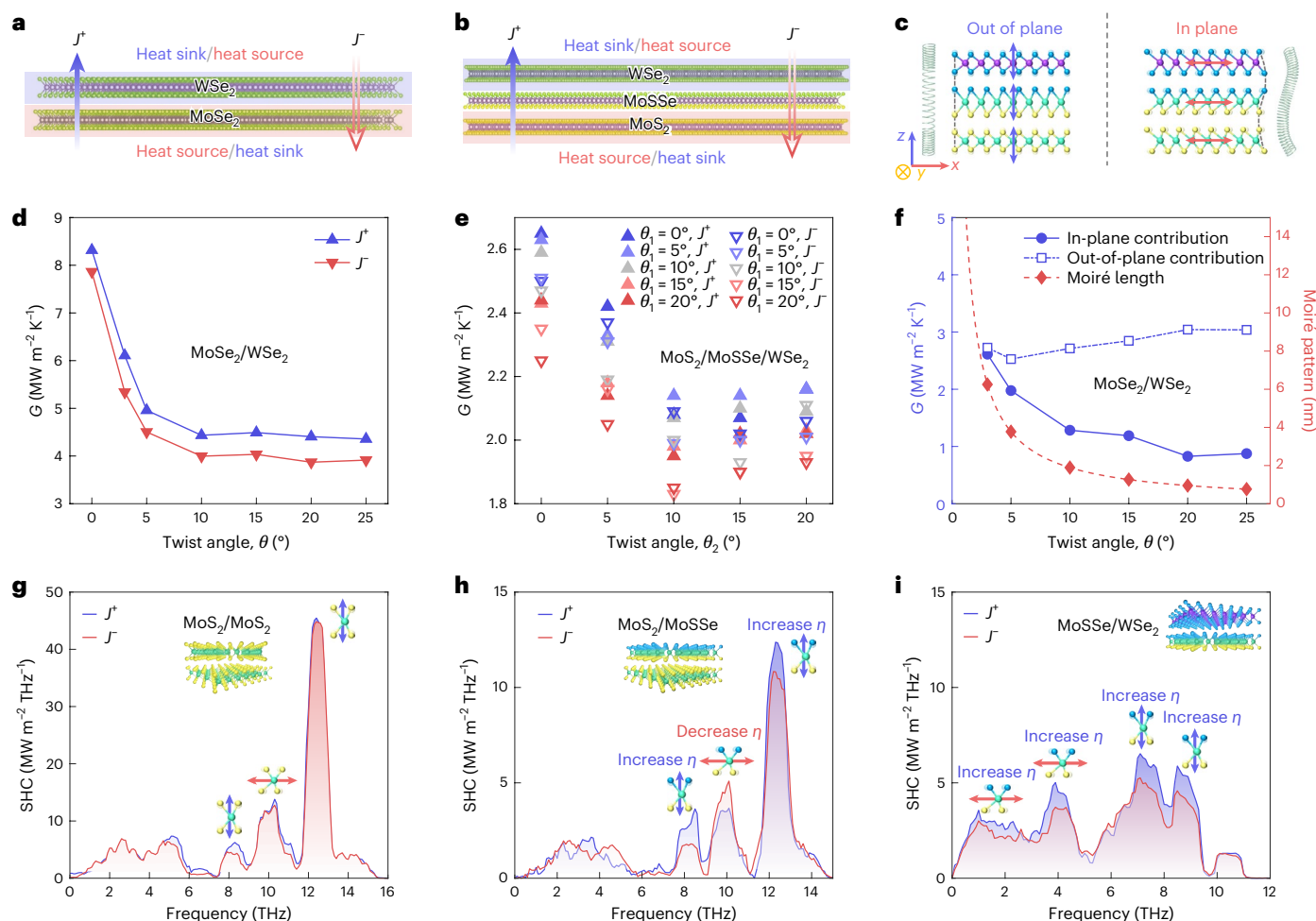


Fig. 4 | Non-equilibrium molecular dynamics simulations revealing the physical mechanisms of asymmetry of heat transport. **a, b**, Schematic of the MoSe₂/WSe₂ (**a**) and MoS₂/MoSe/WSe₂ (**b**) heterostructure models. **c**, Schematic of the contributions of the in-plane and out-of-plane phonon modes to the out-of-plane heat transfer. **d, e**, Variation in the interfacial thermal conductance of the MoSe₂/WSe₂ (**d**) and MoS₂/MoSe/WSe₂ (**e**) heterostructures with the twist angle

in both J^+ and J^- heat flow directions, respectively. **f**, Contributions of in-plane and out-of-plane phonon modes to the interfacial thermal conductance of the MoSe₂/WSe₂ heterostructure. **g–i**, SHC decomposition of the interfacial thermal conductance of the MoS₂/MoSe₂ homointerface (**g**), MoS₂/MoSe heterointerface (**h**) and MoSe/WSe₂ heterointerface (**i**). The shaded region serves to highlight the difference in SHC between J^+ and J^- heat flow conditions.

higher than that in the opposite direction (J^- direction), and the maximum value of η reached up to 29%. However, η showed no apparent angular dependence in the MoSe₂/WSe₂ heterostructure, with its value fluctuating around an average of 19%.

The incorporation of the Janus MoSSe monolayer between MoS₂ and WSe₂ further enhanced the asymmetry of the molecular structure vertically and increased the asymmetry of heat transport. The out-of-plane thermal conductance G of 25 MoS₂/MoSe/WSe₂ trilayer heterostructures were measured at different angles of θ_1 and θ_2 (Fig. 3g). For clarity, the variation in G with θ_2 was extracted at $\theta_1 \approx 0^\circ$ and $\theta_1 \approx 60^\circ$ (Fig. 3f, h, respectively). Evidently, G reached its maximum value when both θ_1 and θ_2 approach 0° , consistent with the measurement results of the MoS₂/MoSe₂ and MoSe₂/WSe₂ bilayer heterostructures. Figure 3e represents a graphical result of the correlation between η and the angles θ_1 and θ_2 . In the heat flow direction from MoS₂ to WSe₂ (J^+ direction), the out-of-plane thermal conductance G of all 25 samples with different angles was higher than that in the opposite direction (J^- direction), with the maximum η reaching 104%. Remarkably, the angular orientation was found to play a pivotal role in the modulation of η . Specifically, η could be effectively modulated by varying the twist angle, with values ranging from 23% (at $\theta_1 = 30.0^\circ$, $\theta_2 = 46.9^\circ$) to 104% (at $\theta_1 = 2.2^\circ$, $\theta_2 = 56.6^\circ$).

Physical mechanisms underlying asymmetry of heat transport

We have conducted non-equilibrium molecular dynamics simulations to elucidate the physical mechanisms of the asymmetry of heat transport. In twisted bilayer MoS₂, MoSe₂/WSe₂ and twisted trilayer MoS₂/MoSe/WSe₂ simulation models, the temperatures of the top and bottom layers were controlled by using Langevin heat baths to induce the vertical temperature gradient (Fig. 4a, b). The simulation results indicated the absence of the asymmetry of heat transport in the twisted bilayer MoS₂ (Supplementary Section 2), as expected due to molecular symmetry. In twisted MoSe₂/WSe₂, heat preferentially flowed from MoSe₂ to WSe₂ (Fig. 4d). The interfacial thermal conductance decreased with increasing twist angle, consistent with the experimental results. In twisted trilayer MoS₂/MoSe/WSe₂, heat predominantly flowed from MoS₂ to WSe₂ (Fig. 4e), and the increasing twist angles θ_1 and θ_2 led to the reduced out-of-plane thermal conductance. Phonon density of states (DOS) overlap is often used to reflect the matching of phonon spectra between two materials, qualitatively characterizing the relative magnitude of interfacial thermal conductance. In twisted MoSe₂/WSe₂, the calculated DOS overlap of the Se atoms closest to the interface was higher when heat flowed from MoS₂ to WSe₂, reflecting the vertical asymmetry of heat transport behaviour (Supplementary Fig. 11).

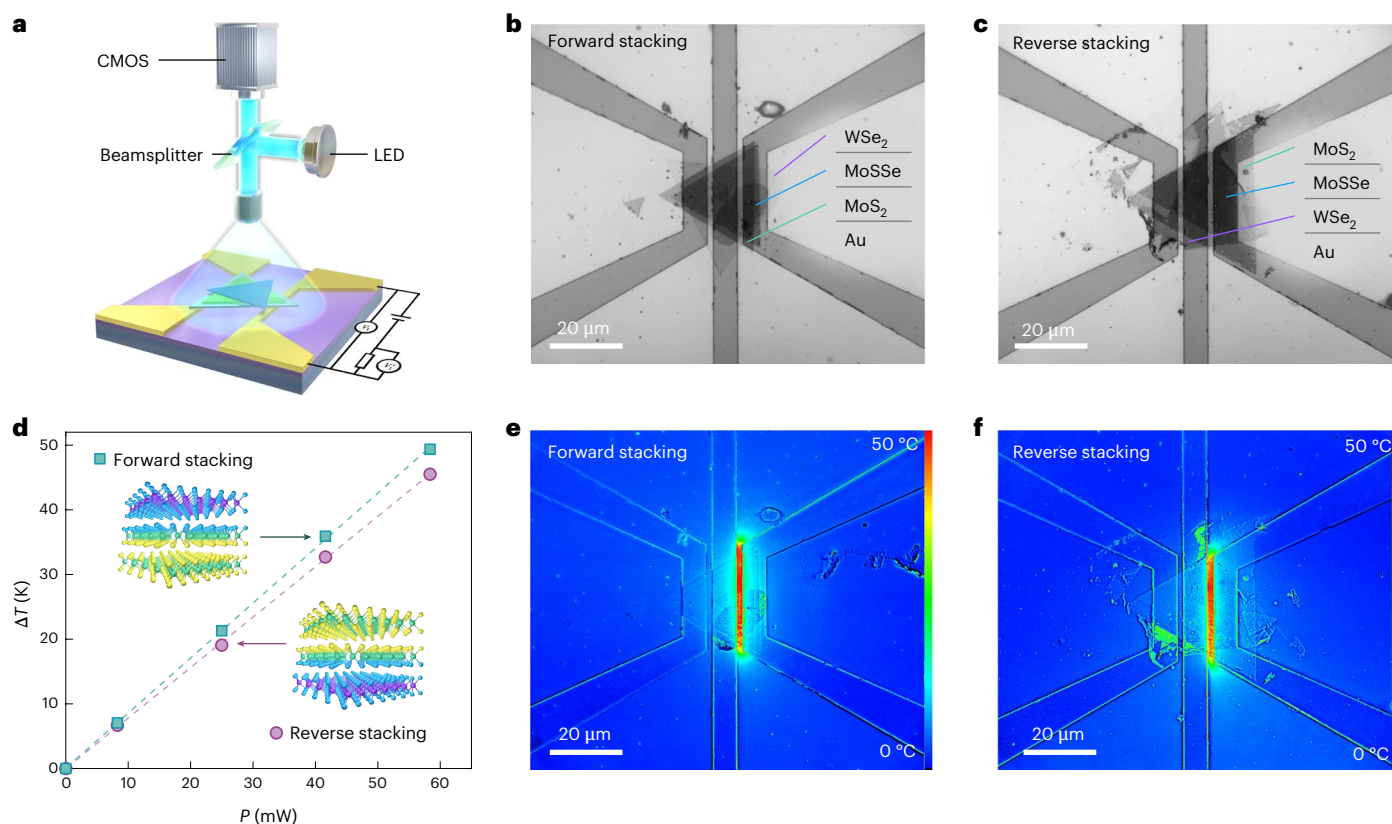


Fig. 5 | Validation of the asymmetry of heat transport effect in FET devices by using the TTI system. **a**, Schematic of the TTI system to measure the surface temperature distribution of the device. The thin gold electrode acts as a Joule heater under the $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ trilayer heterostructure. CMOS, complementary metal–oxide–semiconductor; LED, light-emitting diode. **b**, OM image of the forward-stacked heterostructure on the electrode

(Au/ $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ from bottom to top). **c**, OM image of the reverse-stacked heterostructure on the electrode (Au/ $\text{WSe}_2/\text{MoSSe}/\text{MoS}_2$ from bottom to top). **d**, Surface temperature of the trilayer material directly above the gold electrodes as a function of heating power. **e, f**, Temperature distributions of the structures in **b** (e) and **c** (f) at a heating power of 58 mW.

We have performed spectral heat current (SHC) decomposition to differentiate the contributions of phonons at different frequencies and different vibration modes. The in-plane phonon modes, which are contributed by the movement of atoms in the x or y direction, manifest as transverse waves in the interfacial heat conduction. Conversely, the out-of-plane phonon modes, resulting from the movement of atoms in the z direction, appear as longitudinal waves in the interfacial heat conduction (Fig. 4c). For the $\text{MoSe}_2/\text{WSe}_2$ heterostructure shown in Fig. 4f, with an increasing twist angle, the in-plane component of the interfacial thermal conductance decreased substantially, whereas the out-of-plane component increased slightly. This could be attributed to the reduction in the moiré pattern length L with increasing twist angle, which, in turn, suppresses the transport of long-wavelength phonons. Further frequency-domain decomposition of the SHC confirms this finding. With the increasing twist angle, the low-frequency component of the in-plane SHC decreased, whereas the high-frequency component of the out-of-plane SHC increased (Supplementary Fig. 13). This indicates that larger moiré patterns, characterized by longer periodicity, facilitate the coupling of long-wavelength, low-frequency in-plane phonons. Conversely, smaller moiré patterns, with shorter periodicity, enhance the coupling strength of short-wavelength, high-frequency out-of-plane phonons. Similar results were observed at two interfaces in twisted trilayer $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$, explaining the dependence of interfacial thermal conductance on twist angle.

To further elucidate the asymmetry of the heat transport mechanism, we meticulously investigated the SHC at the interfaces in different heat flow directions. For the $\text{MoS}_2/\text{MoS}_2$ homointerface, the SHC profiles in both J^+ and J^- heat flow directions are essentially coincident

(Fig. 4g), indicating the absence of asymmetry of heat transport effects, as expected. By contrast, we calculated the trilayer $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ heterostructure and extracted the SHC curves for its constituent $\text{MoS}_2/\text{MoSSe}$ and $\text{MoSSe}/\text{WSe}_2$ heterointerfaces. These curves exhibit substantial differences in the J^+ and J^- heat flow directions (Fig. 4h,i): the interfacial thermal conductance at the $\text{MoS}_2/\text{MoSSe}$ interface was predominantly attributed by the high-frequency phonons in the 7–15-THz range, whereas at the $\text{MoSSe}/\text{WSe}_2$ interface, the contribution of low-frequency phonons (0–5 THz) was approximately equal to that of the high-frequency phonons. This discrepancy demonstrates that the existence of Janus material MoSSe creates notable asymmetric SHC at the two heterointerfaces above and below, which is a key factor to the out-of-plane asymmetry of the heat transport effect. Furthermore, the phonon mode analysis revealed that at the $\text{MoS}_2/\text{MoSSe}$ heterointerface, the contribution of out-of-plane phonons to SHC was larger in the J^+ direction than that in the J^- direction, whereas the contribution of in-plane phonons was larger in the J^- direction than that in the J^+ direction, suggesting a competitive relationship between different phonon modes on the asymmetry of heat transport (Supplementary Fig. 14). By contrast, at the $\text{MoSSe}/\text{WSe}_2$ heterointerface, both in-plane and out-of-plane phonons made contributions to the asymmetry of heat transport in the J^+ direction and increased the ratio η . These findings were based on a detailed statistical analysis of 12 different combinations of θ_1 and θ_2 angle configurations (Supplementary Figs. 15–17). Consequently, the pronounced differences in the SHC profiles at the two heterointerfaces, along with the distinct contributions from the in-plane and out-of-plane phonon modes, collectively facilitate asymmetry of heat transport.

Validation of asymmetry of heat transport in real devices

To validate the out-of-plane asymmetry of heat transport effect, we fabricated a field-effect transistor (FET) device with a $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ trilayer on top of two thin gold electrodes. Two heterostructures were prepared for comparison: one with forward stacking ($\text{Au}/\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ from bottom to top) and the other one with reverse stacking ($\text{Au}/\text{WSe}_2/\text{MoSSe}/\text{MoS}_2$ from bottom to top) (Fig. 5a–c). The thin gold electrode ($1.5\ \mu\text{m} \times 30\ \mu\text{m}$, 100 nm thick) was used as a Joule heater. The surface temperature distribution of the device was measured by using a non-contact TTI method (Fig. 5e,f; Supplementary Fig. 6 provides more details). Also, 470-nm-wavelength light from a light-emitting diode was used as the incident light, which has the highest sensitivity to TMD materials. With the $\times 100$ objective lens of the microscope subsystem, the TTI system could achieve a spatial resolution of approximately 260 nm. During the experiment, the heating power P applied to the thin gold electrode was incrementally increased, and the average temperature rise ΔT was calculated within a rectangular area ($1.5\ \mu\text{m} \times 30\ \mu\text{m}$) directly above the heating electrode. The results demonstrated that ΔT exhibited a favourable linear relationship with P , regardless of the stacking order of TMD materials (Fig. 5d), consistent with the theoretical predictions. Owing to the relatively lower out-of-plane thermal conductance, for a given heating power, the surface temperature of reverse-stacked heterostructure ΔT_{Rev} was lower than that of the forward-stacked heterostructure ΔT_{For} . At a heating power of 58 mW, the difference $\Delta T_{\text{For}} - \Delta T_{\text{Rev}}$ reached 3.9 K. Through finite element analysis, we quantitatively determined that 90.5% of this temperature difference is attributable to the asymmetry in interfacial thermal conductance (Supplementary Section 1). This finding confirms the notable asymmetry of the heat transport effect of the trilayer heterostructure, consistent with the experimental results shown in Fig. 3. Considering the linear relationship between ΔT and P , the temperature difference $\Delta T_{\text{For}} - \Delta T_{\text{Rev}}$ became larger at a higher heating power. This confirmed that the multilayer heterostructure can be used as an intelligent thermal interfacial material. When the temperature of the core device reaches a high value, the heterostructure material on top facilitates rapid heat dissipation into the surroundings. Conversely, when the temperature of the core device is lower than that of the surroundings, the temperature gradient inverts and the thermal conductance decreases, protecting the core device from outside detrimental heat influx.

Conclusions

We have reported the fabrication of a $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ trilayer thermal Janus crystal that offers an asymmetric phonon transport property exceeding 90%. For chips containing multiple interfaces, integrations of this heterostructure could provide both rapid cooling and insulation from external high-temperature damage. The sub-5-nm thickness is also favourable for chip integration. When placed on top of a FET device, the heterostructure material generated a temperature difference of 3.9 K at the surface due to the asymmetric thermal transport effect. The variable stacking order and controllable interfacial twist angles of the heterostructure also offers flexibility for modulating moiré superlattices, interlayer coupling, SHC and band structures, which could potentially be used to achieve unidirectional thermal conduction.

Methods

Synthesis of MoS_2 , MoSe_2 , WSe_2 and Janus MoSSe monolayers

The fused silica and sapphire substrates purchased from Dongda Times, Chengdu Technology, were first pretreated with O_2 plasma. The metal precursors were predeposited on the fused silica piece by spin coating corresponding molten-salt aqueous solutions with optimized concentrations (12 mg ml^{-1} of Na_2MoO_4 , 18 mg ml^{-1} of Na_2MoO_4 and 30 mg ml^{-1} of Na_2WO_4 for the synthesis of MoS_2 , MoSe_2 and WSe_2 , respectively). Then, the precursor-coated pieces were heated at 80 °C for drying. The precursor-coated fused silica, sapphire substrate and

chalcogenide crystal plate (ZnS or ZnSe) were stacked in a face-to-face manner by a customized holder. This integrated system was placed on a quartz plate and loaded in the chemical vapour deposition furnace, elevating to the optimized temperature for material growth (780 °C, 800 °C and 820 °C for the synthesis of MoS_2 , MoSe_2 and WSe_2 , respectively). The chamber pressure was kept at ~ 120 Pa with 100-s.c.c.m. Ar during the growth. After growth, the furnace was naturally cooled down to room temperature. The Janus MoSSe monolayer was fabricated by the post-processing of as-grown MoS_2 on *a*-plane sapphire substrate. The MoS_2 monolayers and ZnSe crystal plates were placed face to face and loaded into the chemical vapour deposition furnace. The Se substitution was carried out at 870 °C at low pressure with 120-s.c.c.m. Ar and 5-s.c.c.m. H_2 . This high-temperature process was kept for 60 min to ensure the full substitution of the top-layer S atoms of MoS_2 . The system was naturally cooled down to room temperature after substitution.

Fabrication of X-shaped sensors and $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ heterostructures

Fabrication of the bottom sensor and electrode pad. To achieve the required dimensional precision for the sensing device, electron-beam lithography was utilized to pattern the sensor and electrode pad with a resolution exceeding 10 nm. A 300-nm-thick layer of electron-beam (EB) resist (ZEP520A) was spin coated onto a silicon wafer coated with a 50-nm-thick SiO_2 layer, which was subsequently patterned into the bottom sensor and electrode pattern. Following this, a 50-nm-thick gold (Au) layer, preceded by an 8-nm-thick chromium adhesion layer, was deposited onto the patterned wafer via electron-beam PVD.

Pretreatment of monolayer TMD samples for transfer. The monolayer TMD samples, including WSe_2 , MoS_2 and MoSSe , were initially synthesized on fused silica or sapphire substrates via the chemical vapour deposition method, as mentioned above. To reduce the adhesion force between the TMD material and substrate to facilitate subsequent transfer, the TMD samples were transferred to a clean SiO_2 substrate by using a typical PMMA method. First, a 300-nm-thick PMMA layer was spin coated on top of the TMD material. Then, the coated samples were immersed in a 10% sodium hydroxide solution for a duration of 1–3 h to etch the sapphire substrate and release the TMD/PMMA layer. After that, the samples were thoroughly rinsed with ultrapure water to remove chemical residuals. Finally, the TMD/PMMA layer was transferred onto a clean SiO_2 substrate, ready for the next transfer process.

Transfer of monolayer TMD samples. Subsequent to the pretreatment, we used the micromanipulation system (MicroSupport, AxisPro SS) to transfer a triangular single-crystalline MoS_2 domain to the SiO_2/Si substrate with pre-prepared bottom gold sensors and electrode. The position (1- μm accuracy) and orientation (0.3° precision) of each transferred MoS_2 crystal domain were meticulously adjusted by using two microprobes (TP-001) with a sharp tip of around 1 μm in diameter. After that, the substrate was heated to 135 °C and maintained at this temperature for 10 min on a temperature-controlled stage to enhance the adhesion force between the monolayer TMD and the substrate. Then, the sample was immersed in butanone at 45 °C for 30 min to fully dissolve the PMMA layer, thereby establishing an Au/MoS_2 stacking structure. To further remove possible chemical residues left by the dissolved PMMA, the sample was annealed at 400 °C for more than 8 h within a mixed atmosphere of hydrogen and argon under a slow flow rate. The good quality and cleanness of the crystalline structure can be ensured by the clear aberration-corrected TEM images of both cross-section (Fig. 1n) and moiré patterns (Fig. 1h–j). The same transfer process was repeated twice to transfer single-crystalline MoSSe and WSe_2 domains sequentially, forming a trilayer TMD heterostructure $\text{MoS}_2/\text{MoSSe}/\text{WSe}_2$ (the complete transfer process of the trilayer TMD sample is shown in Supplementary Video 1).

Fabrication of top gold sensor and electrode pad. After the transfer process in step (3), the same electron-beam lithography methodology delineated in step (1) was utilized to pattern the top gold sensor and electrode pad. Then, a 50-nm-thick Au layer was deposited by electron-beam PVD on the trilayer TMD sample to serve as the top sensor. The top and bottom sensors formed an X shape with the trilayer TMD sample in between, forming a stacked structure of Au/MoSe₂/MoSSe/WSe₂/Au.

Fabrication of the suspended structure. After fabrication of the top sensor and electrode pad, a 300-nm-thick layer of EB resist was spin coated on the surface and patterned into a 5 $\mu\text{m} \times 5 \mu\text{m}$ square, whose centre coincided with the centre of the X-shaped sensors. Subsequently, the 50-nm-thick SiO₂ layer uncovered by EB resist was removed by using a reactive ion etching method with a CHF₃/Ar gas mixture. After that, the sample was put into a XeF₂ gas etching reactor for isotropic etching of the exposed Si substrate. The EB resist and top sensor protected the TMD sample from the etching gas. An etching depth of approximately 8 μm was sufficient to fully suspend the whole X-shaped sensors and 5 $\mu\text{m} \times 5 \mu\text{m}$ -wide sensing area, guaranteeing the highest measurement sensitivity for the interfacial thermal conductance of the trilayer TMD heterostructure.

Removal of EB resist. The chip was finally immersed in butanone at a temperature of 45 °C for 30 min to remove the EB resist layer on the surface. After that, the chip with suspended X-shaped sensors was dried by using a supercritical point drying system (Leica EM CPD300) to avoid surface-tension-induced damage to the sensors or TMD sample. On completion of these steps, the sensing device was ready to measure the out-of-plane thermal conductance of TMD heterostructures. The fabrication process was the same for the monolayer MoSe₂ sample and bilayer MoSe₂/WSe₂ sample with suspended X-shaped sensors.

Out-of-plane thermal conductance measurement method

The accurate measurement of out-of-plane thermal conductance necessitates the determination of both temperature gradient across the sample and the heat flux passing through it. In this study, two suspended gold nanosensors were used as individual precise resistance thermometers to quantify the temperature gradient. The resistance–temperature coefficient of the gold nanosensor was carefully calibrated beforehand. Utilizing a vacuum thermoelectric heating and cooling stage (INSTECH, TP102VF-MPS-6PB, ± 0.001 K), the ambient temperature of the sample can be accurately controlled. By changing the electrical current applied to the X-shaped sensor, the corresponding data of the resistance and electrical power were acquired. A resistance–power curve was fitted to extrapolate the resistance of the sensor at zero current, which corresponds to the resistance at ambient temperature. Subsequently, by changing the ambient temperature of the stage, a resistance–temperature curve was constructed for each sensor.

In the current research, to measure the thermal conductance in the J^* direction, the bottom gold nanosensor was subjected to a high current across its terminals, thereby functioning as a heater to generate Joule heating power P . A portion of this heat, quantified as Q , was conducted through the heterostructure in the out-of-plane direction to the other nanosensor on top. To quantify this thermal conduction process, a proportionality coefficient was defined as α :

$$\alpha = \frac{Q}{P}. \quad (1)$$

Concurrently, a weak current was applied to the top sensor across terminals, serving as a detector. By monitoring the resistance change of the two sensors, their average temperature rises could be measured and the difference was denoted as $\Delta T_{\text{Average}}$. To determine the thermal conductance of the sample located in the centre of the two X-shaped

sensors, it is necessary to calculate the temperature difference ΔT_{Centre} at the overlapping central area of both top and bottom sensors. The proportionality coefficient of these two temperature differences is denoted as β :

$$\beta = \frac{\Delta T_{\text{Centre}}}{\Delta T_{\text{Average}}}. \quad (2)$$

On the establishment of the above temperature difference and heat flux, the out-of-plane thermal conductance in the J^* direction can be determined as follows:

$$G_{\text{eff}} = \frac{Q}{A\Delta T_{\text{Centre}}} = \frac{\alpha P}{\beta A\Delta T_{\text{Average}}}, \quad (3)$$

where A represents the overlapping area between the two sensors (approximately 1 μm^2). P can be accurately obtained by measuring the current and voltage applied to the heater sensor. $\Delta T_{\text{Average}}$ can be measured by assessing the resistances of both heater and detector sensors.

To obtain the proportionality coefficients α and β in the above equations, a steady-state heat conduction simulation model was established in COMSOL Multiphysics. It is unambiguous that the shape of the electrode influences the coefficients α and β . Therefore, the geometric dimensions of sensor and electrode were precisely extracted from the scanning electron microscopy (SEM) images of suspended sensing device (Supplementary Fig. 4a) to establish the simulation model (Supplementary Fig. 4b,c). The thermal conductivities of gold sensor and silicon dioxide were measured and calibrated using the methods introduced in our previous work^{33,41}. As a result, the out-of-plane thermal conductance G_{eff} was the only unknown parameter to be determined. Both α and β were uniquely influenced by G_{eff} , such that $\alpha = \alpha(G_{\text{eff}})$ and $\beta = \beta(G_{\text{eff}})$. Then, the aforementioned equation can be reformulated as follows:

$$\frac{P}{A\Delta T_{\text{Average}}} = \frac{\beta G_{\text{eff}}}{\alpha}. \quad (4)$$

Given the out-of-plane thermal conductance G_{eff} , the ratio $(\beta G_{\text{eff}})/\alpha$ can be calculated from the heat conduction simulation by COMSOL, allowing us to plot G_{eff} versus $P/(A\Delta T_{\text{Average}})$ curve (Supplementary Fig. 4d). On the basis of this curve, the out-of-plane thermal conductance G_{eff} can be solely determined from the corresponding value of $P/(A\Delta T_{\text{Average}})$ measured in the experiment.

Molecular dynamics simulation

All molecular dynamics simulations were conducted using the LAMMPS software⁴². The intramolecular interactions within the TMD layer were described using a modified Stillinger–Weber potential⁴³, whereas the interlayer interactions between TMD molecules were modelled using the interlayer potential⁴⁴. To simulate the interfacial thermal conductance, we first minimized the system's energy. The system was relaxed in an NPT ensemble at 300 K for 100 ps without external pressure to release residual stress. Subsequently, the system was further relaxed in an NVE ensemble for 100 ps to reach a stable state. Non-equilibrium molecular dynamics simulations were then performed for 5 ns, with the temperatures of the heat source and heat sink controlled by Langevin thermostats, where the damping parameter was set to 100 ps.

To calculate the vibrational DOS, we extracted the velocities of the two atomic layers closest to the interface. For the WSe₂/MoSe₂ heterojunction, these were the top Se atoms in WSe₂ and the bottom Se atoms in MoSe₂ (Supplementary Fig. 11a). The vibrational DOS was obtained by performing a fast Fourier transform on the velocity autocorrelation function, given by the following equation:

$$G(\omega) = \frac{1}{\sqrt{2\pi}} \int_0^{t_0} \frac{\langle v(0)v(t) \rangle}{\langle v(0)v(0) \rangle} \exp(i\omega t) dt, \quad (5)$$

where $G(\omega)$ is the normalized vibrational DOS at frequency ω , $v(t)$ is the atomic velocity at time t , τ_0 is the integration time and the angle brackets denote ensemble averaging.

To calculate the spectral heat flux, we export the velocities and force vectors of all atoms every 10 fs. The force–velocity cross-correlation function is defined as follows:

$$K_{ji}(t_1 - t_2) = \frac{1}{2} \langle \mathbf{F}_{ji}(t_1) \cdot \mathbf{v}_j(t_2) - \mathbf{F}_{ij}(t_1) \cdot \mathbf{v}_i(t_2) \rangle. \quad (6)$$

The cross-correlation function is transformed into the frequency domain via Fourier transform:

$$\tilde{K}_{ji}(\omega) = \int_{-\infty}^{\infty} e^{i\omega t} K_{ji}(t) dt. \quad (7)$$

The spectral heat flux is represented as

$$q_{i \rightarrow j}(\omega) = 2\text{Re}[\tilde{K}_{ji}(\omega)]. \quad (8)$$

The total heat flux is represented as

$$Q_{i \rightarrow j} = \int_0^{\infty} \frac{1}{2\pi} q_{i \rightarrow j}(\omega) d\omega. \quad (9)$$

By decomposing the force–velocity cross-correlation function into in-plane and out-of-plane components, we can determine the contributions of in-plane and out-of-plane vibrations to the heat flux:

$$Q_{\text{in plane}} = \frac{1}{2\pi} \int_0^{\infty} d\omega \text{Re} \left[\int_{-\infty}^{\infty} dt e^{i\omega t} (\mathbf{F}_{ji,x}(t) \cdot \mathbf{v}_{j,x}(0) - \mathbf{F}_{ij,x}(t) \cdot \mathbf{v}_{i,x}(0) + \mathbf{F}_{ji,y}(t) \cdot \mathbf{v}_{j,y}(0) - \mathbf{F}_{ij,y}(t) \cdot \mathbf{v}_{i,y}(0)) \right], \quad (10)$$

$$Q_{\text{out of plane}} = \frac{1}{2\pi} \int_0^{\infty} d\omega \text{Re} \left[\int_{-\infty}^{\infty} dt e^{i\omega t} (\mathbf{F}_{ji,z}(t) \cdot \mathbf{v}_{j,z}(0) - \mathbf{F}_{ij,z}(t) \cdot \mathbf{v}_{i,z}(0)) \right]. \quad (11)$$

Data availability

The data supporting the findings of this study are available from the corresponding authors upon reasonable request. Source data are provided with this paper.

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Acknowledgements

This work was supported by the National Key Research and Development Program of China (grant number 2023YFB4404100 to H.W.) and the National Natural Science Foundation of China (grant numbers 52425601 to B.C., 52327809 to B.C., 52276072 to H.W., 92577201 to K.L. and 12427806 to K.L.).

Author contributions

H.W., K.L. and B.C. supervised the project. G.X. grew the MoSSe materials and performed the polarization-resolved SHG characterization. Q.G. performed the TEM characterization. H.Z. and H.W. designed and fabricated the X-shaped sensing device,

prepared the three-layer heterojunction samples, and performed the electrical and thermal property measurements and data analysis. H.Z. performed the Raman spectroscopy mapping and finite element simulations. H.Y. performed the molecular dynamics simulations and wrote the discussion. H.W. fabricated the FETs and Z.L. characterized them using the TTI system. H.Z., H.W., H.Y. and Z.L. wrote the manuscript with the participation of all authors. All authors participated in the discussion of the results and in the revision of the manuscript.

Competing interests

The authors declare no competing interests.

Additional information

Supplementary information The online version contains supplementary material available at <https://doi.org/10.1038/s41928-026-01620-5>.

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Peer review information *Nature Electronics* thanks Zlatan Aksamija, Cheng-Wei Qiu and the other, anonymous, reviewer(s) for their contribution to the peer review of this work.

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